

RELIABILITY MONITOR REPORT
FOR

SipStik Package

Dallas Semiconductor

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This Report was prepared by
Dallas Semiconductor Reliability Engineering

Summary:

The data in the tables that follow was generated as the result of an on-going Package Reliability Monitor. The assemblies covered by this package monitor are:

ASSY SITE	PINS	PACKAGE		
ATEC	72	SipStik	ATEC	80 SipStik
Dallas	80	SipStik	Dallas	80 SipStik w/Conformal Coat
Fastech	80	SipStik	Fastech	80 SipStik w/Conformal Coat

The calculated failure rate for this assembly is:

The parameters used to calculate this failure rate are as follows:

Cf: 60% **Ea: 0.7** **B: 0** **Tu: 25 °C** **Vu: 5.5 Volts**

The reliability data follows. At the start of this data is a description of the assembly vehicle used to generate this reliability data. The next section is the detailed reliability data for each stress. The reliability data section includes the latest data available. This report covers data between 1/1/04 and 12/31/04 .

Assembly Information:

Package Type: SipStik 0 mil
Flammability: UL 94-V0
Moisture Sensitivity
(JEDEC J-STD20A)
Date Code Range: 0351 to 0351

TEMPERATURE CYCLE

DESCRIPTION	DATE CODE	TEST VEHICLE	CONDITION	READPOINT	QUANTITY	FAILS	FA NO
TEMP CYCLE	0351	DS38452	-40C TO +125C	500	CYS	60	0
EXTERNAL VISUAL			JESD22-B101			60	0
					Total:	0	